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S6	130	(replay\$3 or reissu\$3 or re?execut\$3 or (execut\$3 adj1 again) or re?process\$3) same ((track\$3 or count\$3 or follow\$3) with (tim\$3 near4 period\$1))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/08 09:52
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S8	3	(replay\$3 or reissu\$3 or re?execut\$3 or (execut\$3 adj1 again) or re?process\$3) same ((track\$3 or count\$3 or follow\$3) same (tim\$3 near4 period\$1 near4 (increas\$3 or add\$3)))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/08 09:55
S9	1	(replay\$3 or reissu\$3 or re?execut\$3 or (execut\$3 adj1 again) or re?process\$3) same ((track\$3 or count\$3 or follow\$3) same (tim\$3 near4 period\$1 near4 (elongat\$3)))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/08 09:55

S10	20	"replay".ti. and ("712").clas.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/02/08 10:53
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Kim, S.; Vijaykrishnan, N.; Irwin, M.J.; John, L.K.;

Low Power Electronics and Design, 2003. ISLPED '03. Proceedings of the 2003 International Symposium on , 25-27 Aug. 2003

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2 SSD: an affordable fault tolerant architecture for superscalar processors

Seongwoo Kim; Somani, A.K.;

Dependable Computing, 2001. Proceedings. 2001 Pacific Rim International Symposium on , 17-19 Dec. 2001

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3 Memory Ordering: A Value-Based Approach

Cain, H.W.; Lipasti, M.H.;

Micro, IEEE , Volume: 24 , Issue: 6 , Nov.-Dec. 2004

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IEEE JNL

4 Fault tolerance through re-execution in multiscalar architecture

Rashid, F.; Saluja, K.K.; Ramanathan, P.;

Dependable Systems and Networks, 2000. DSN 2000. Proceedings International Conference on , 25-28 June 2000

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5 Cyclone: a broadcast-free dynamic instruction scheduler with selective replay

Ernst, D.; Hamel, A.; Austin, T.;

Computer Architecture, 2003. Proceedings. 30th Annual International Symposium
on , 9-11 June 2003
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